



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 3972

Bib Data Sheet

SERIAL NUMBER 09/691,353	FILING DATE 10/18/2000 RULE	CLASS 438	GROUP ART UNIT 2823	ATTORNEY DOCKET NO. BUR9-1999-0300-US1
-----------------------------	---------------------------------------	--------------	------------------------	---

APPLICANTS

James W. Addisson, Jericho, VT;

Paul D. Angello, Wappingers Falls, NY;

Arne W. Ballantine, Round Lake, NY; Rama Divakaruni, Somers, NY;

Erin C. Jones, Tuckahoe, NY;

Edward J. Nowak, Essex Junction, VT;

Jed H. Rankin, Burlington, VT;

** CONTINUING DATA ***** none K.N. IBM

** FOREIGN APPLICATIONS ***** none K.N.

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/01/2000

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <u>K. N. Rangarajan</u> Examiner's Signature Initials	VT	10	23	3

ADDRESS

30743

WHITHAM, CURTIS & CHRISTOFFERSON, P.C.

11491 SUNSET HILLS ROAD

SUITE 340

RESTON, VA

20190

TITLE

Method of fabricating semiconductor side wall fin

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
------------	---	---